

FIG. 1A

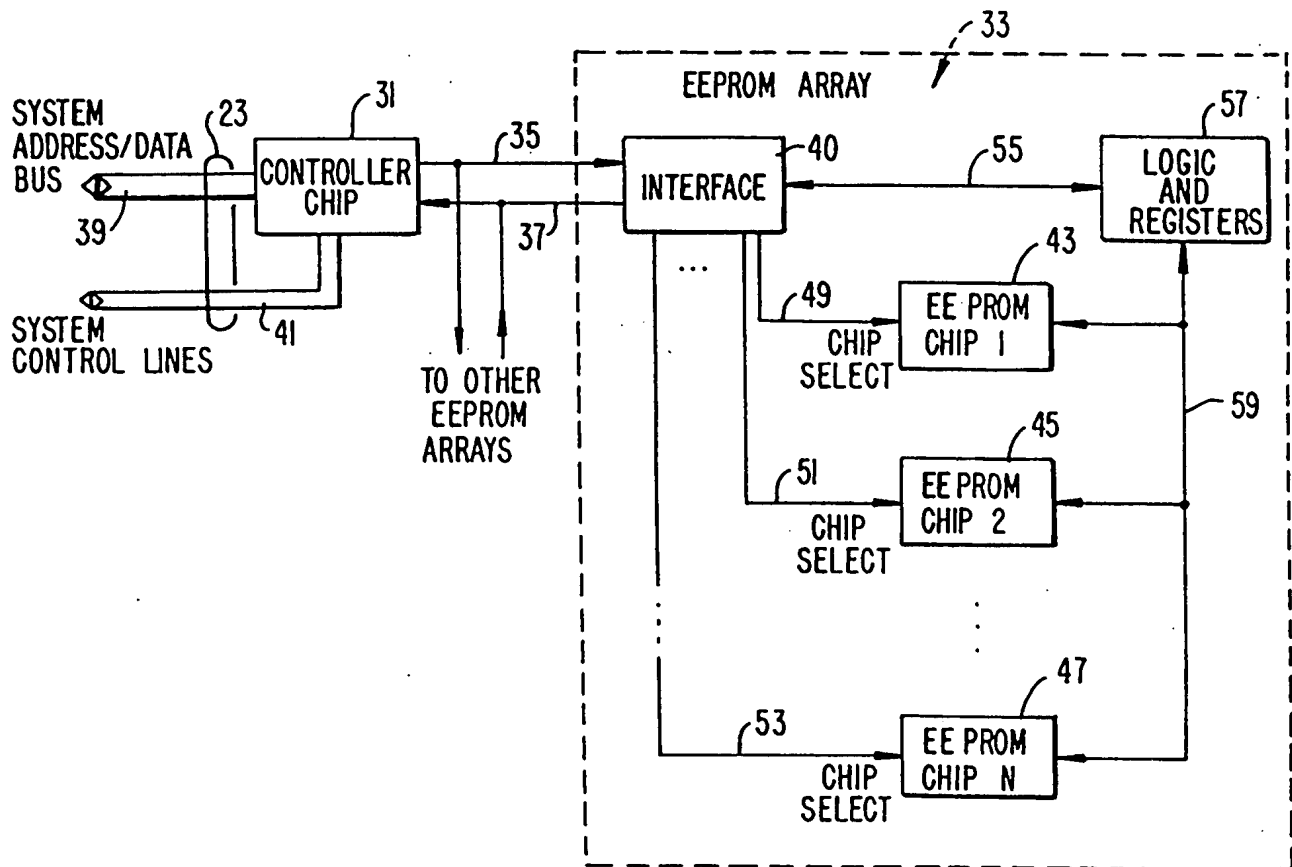


FIG. 1B



FIG._3A

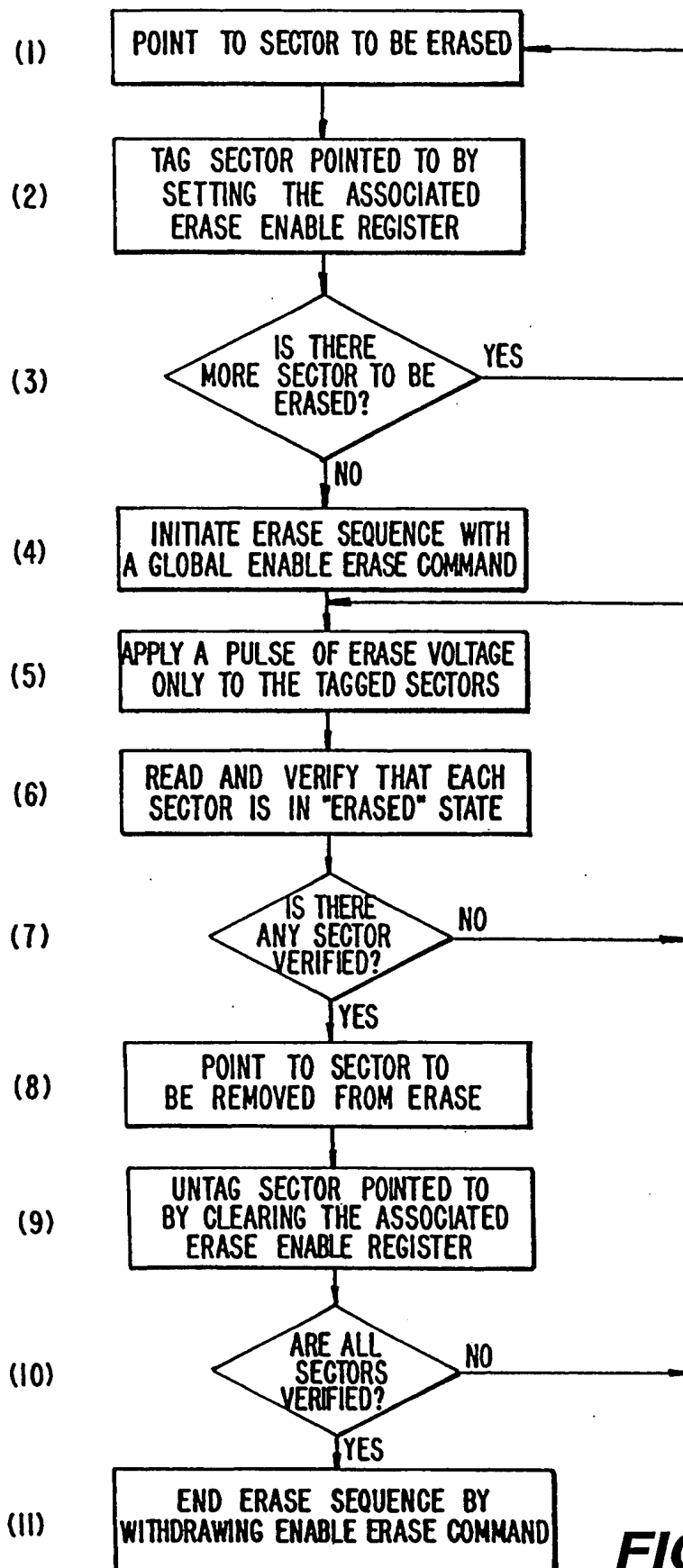


FIG. 4

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FIG. 5

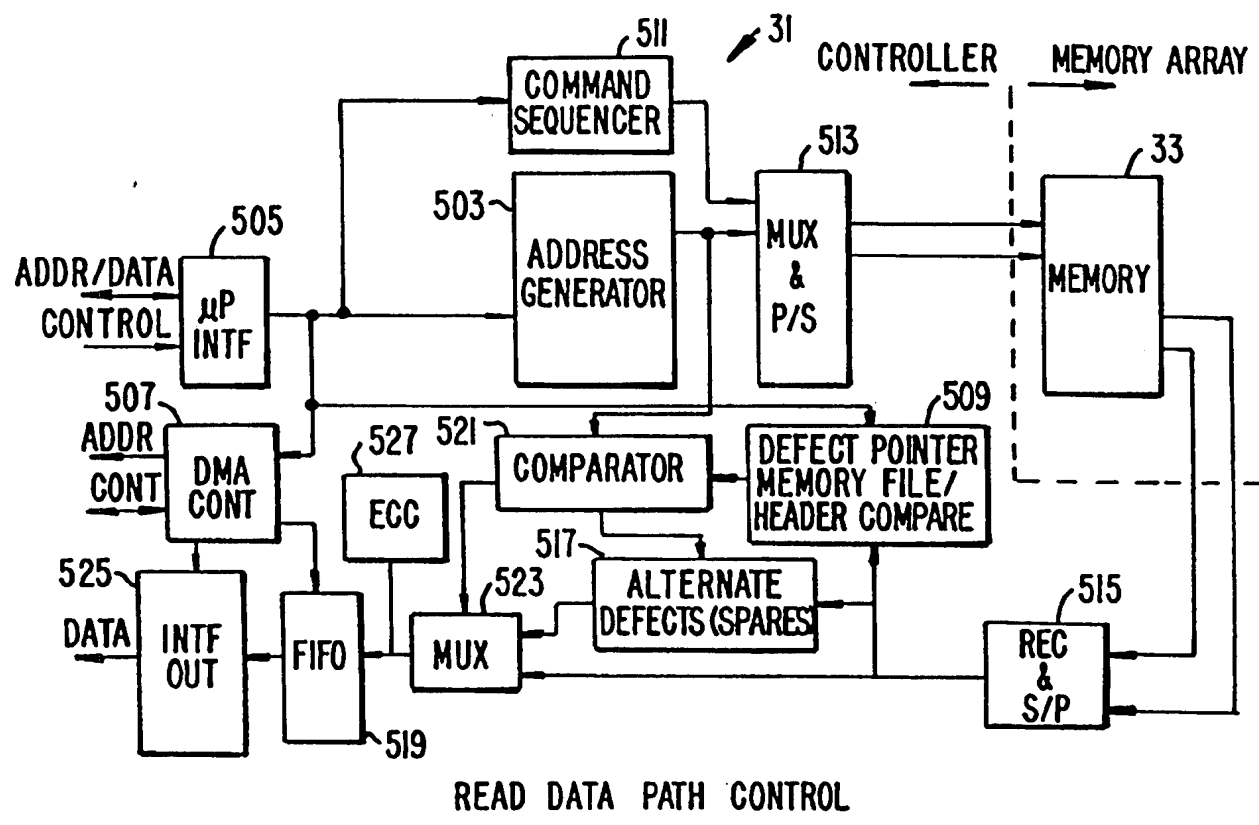
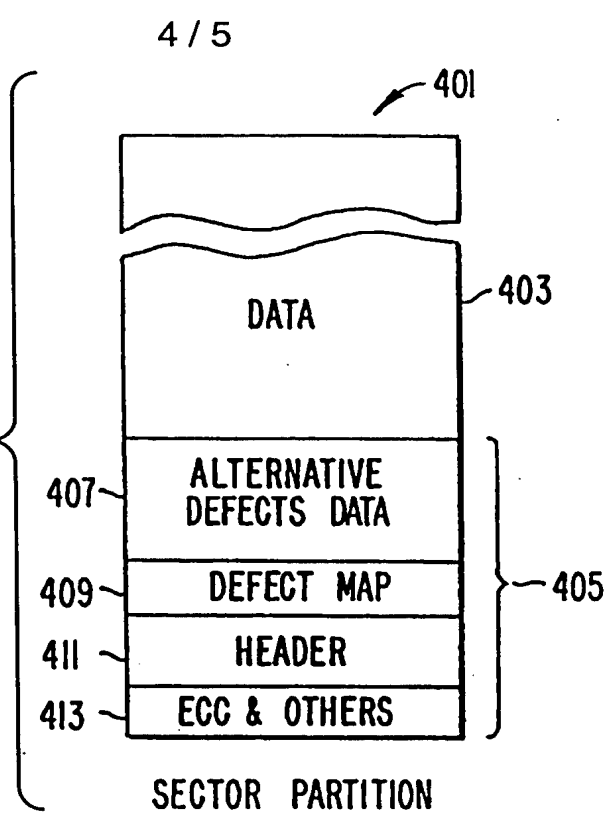
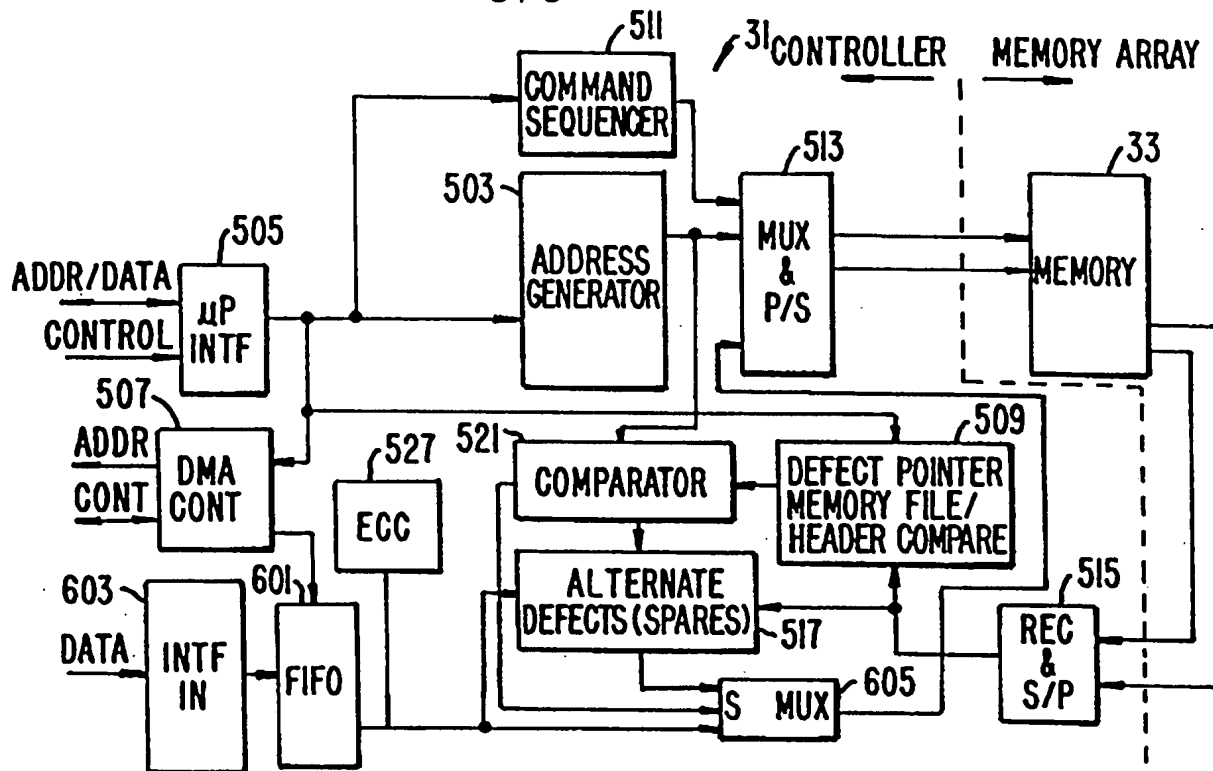


FIG. 6

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WRITE DATA PATH CONTROL

FIG. 7

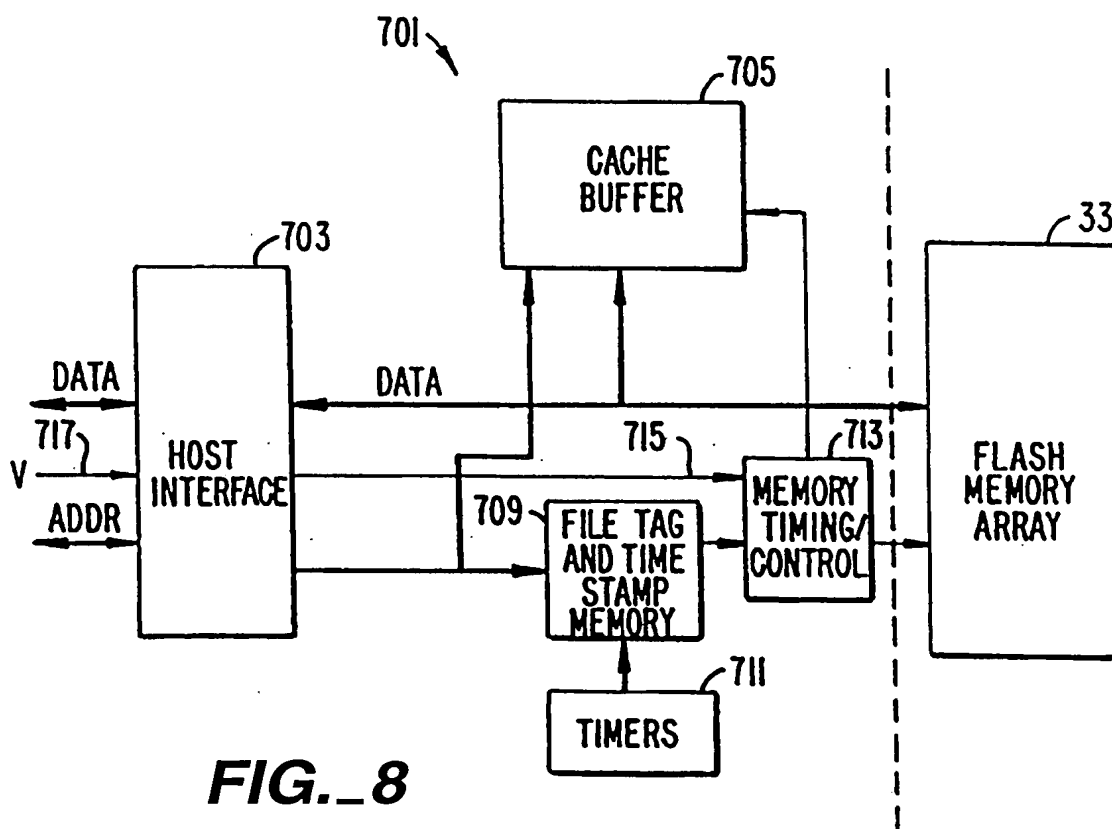


FIG. 8

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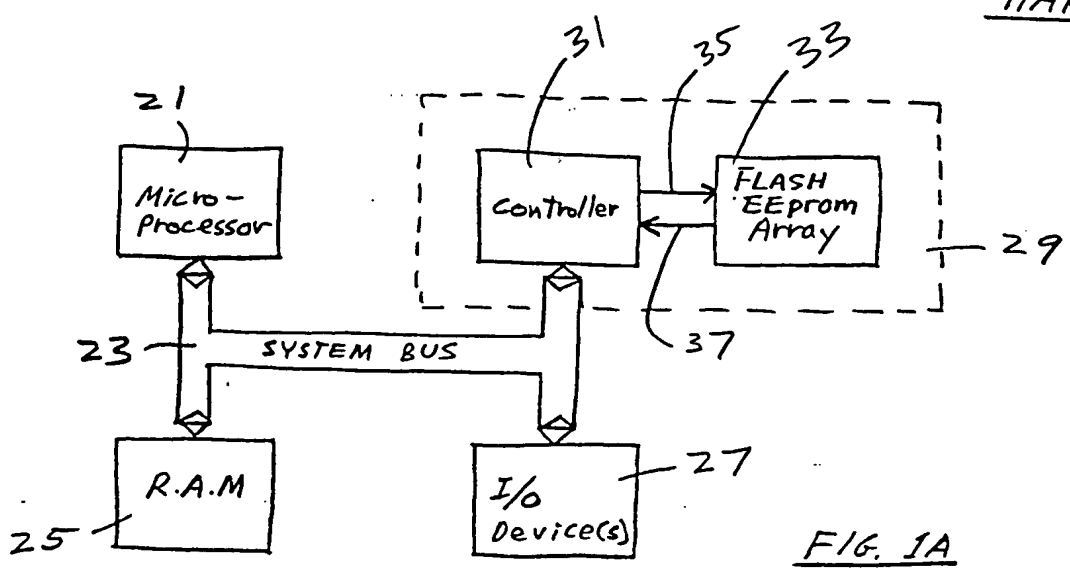


FIG. 1A

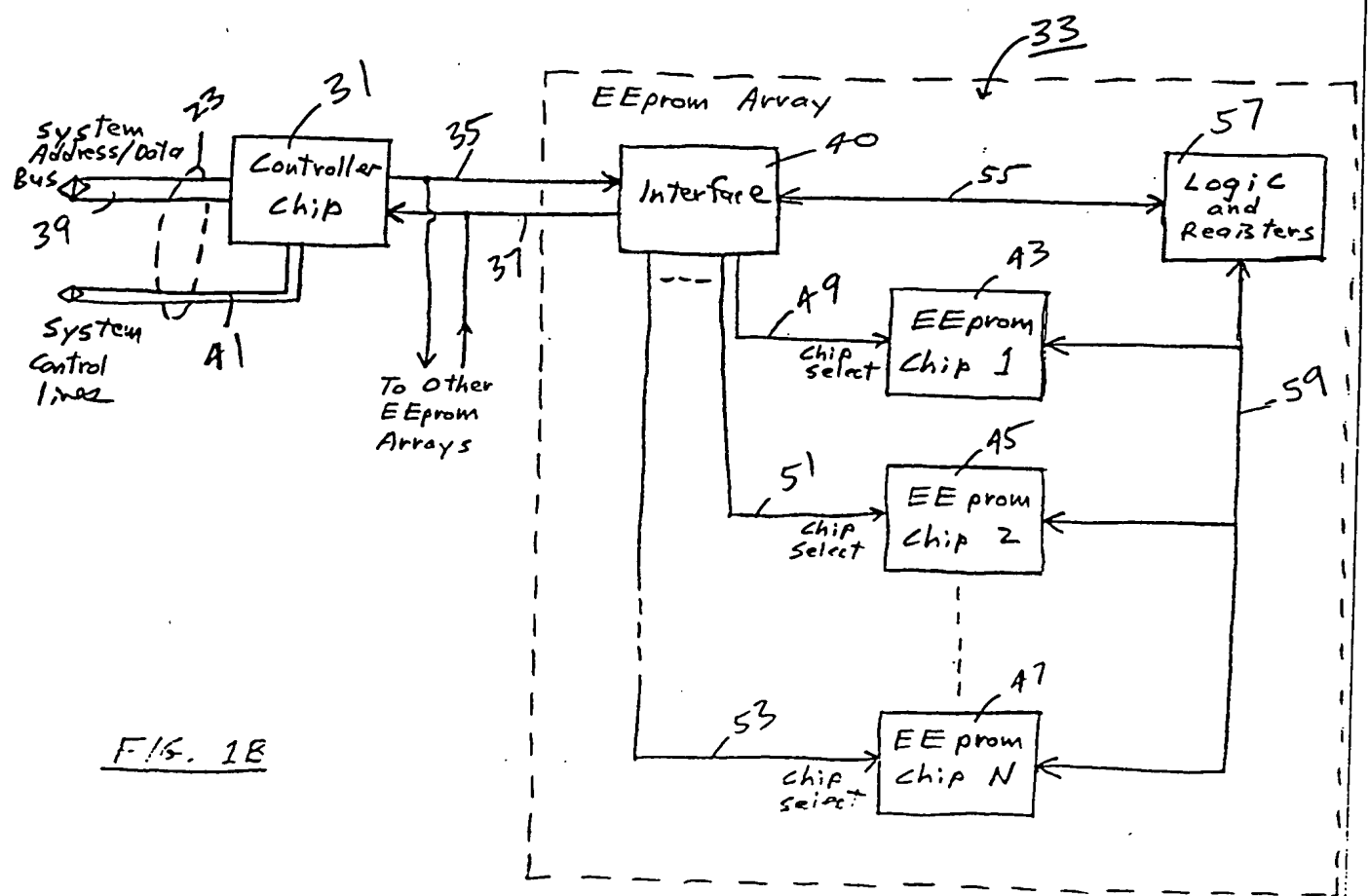
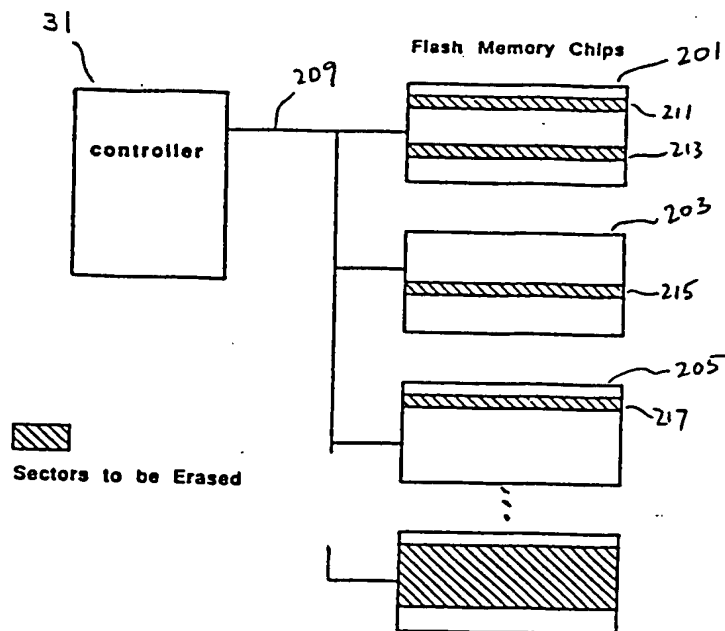


FIG. 1B



FIG_2

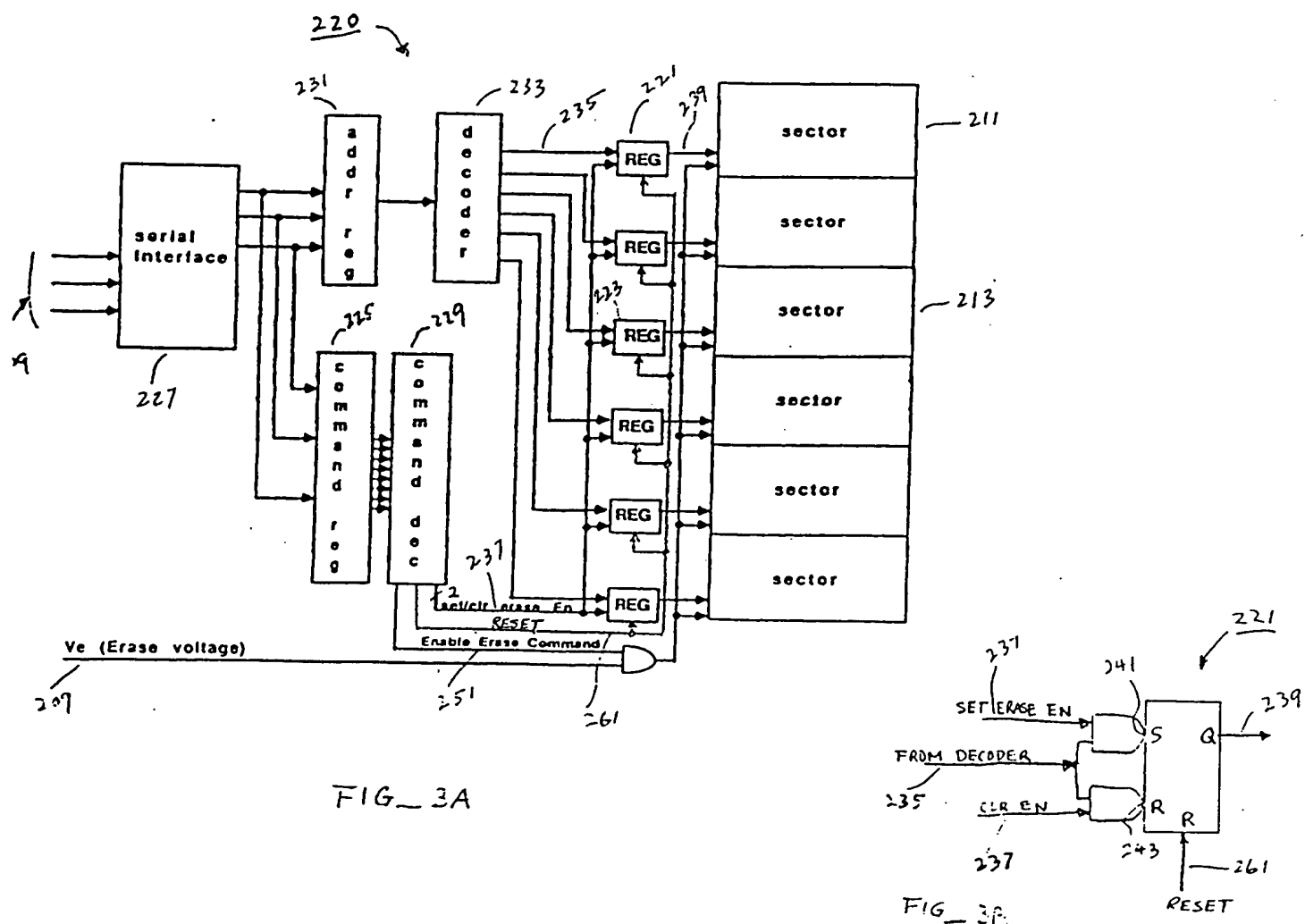


FIG-3c

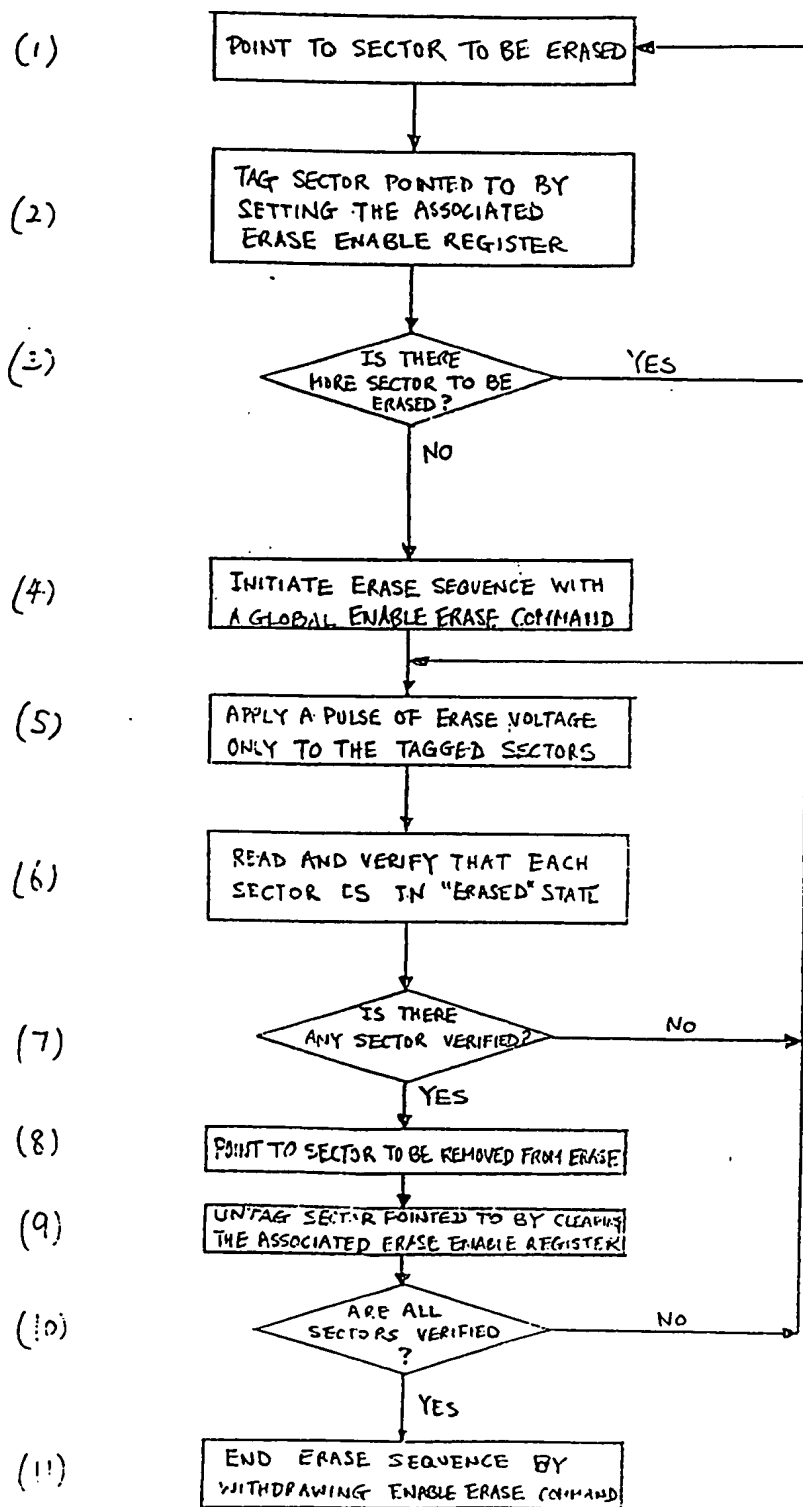
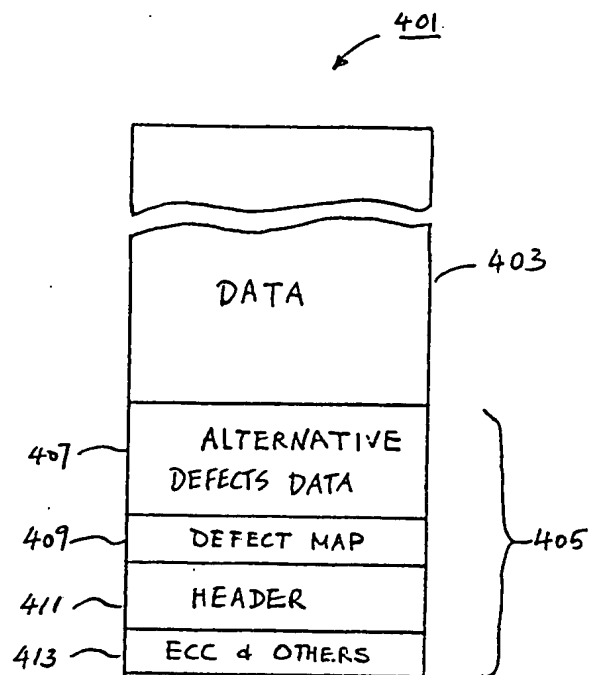
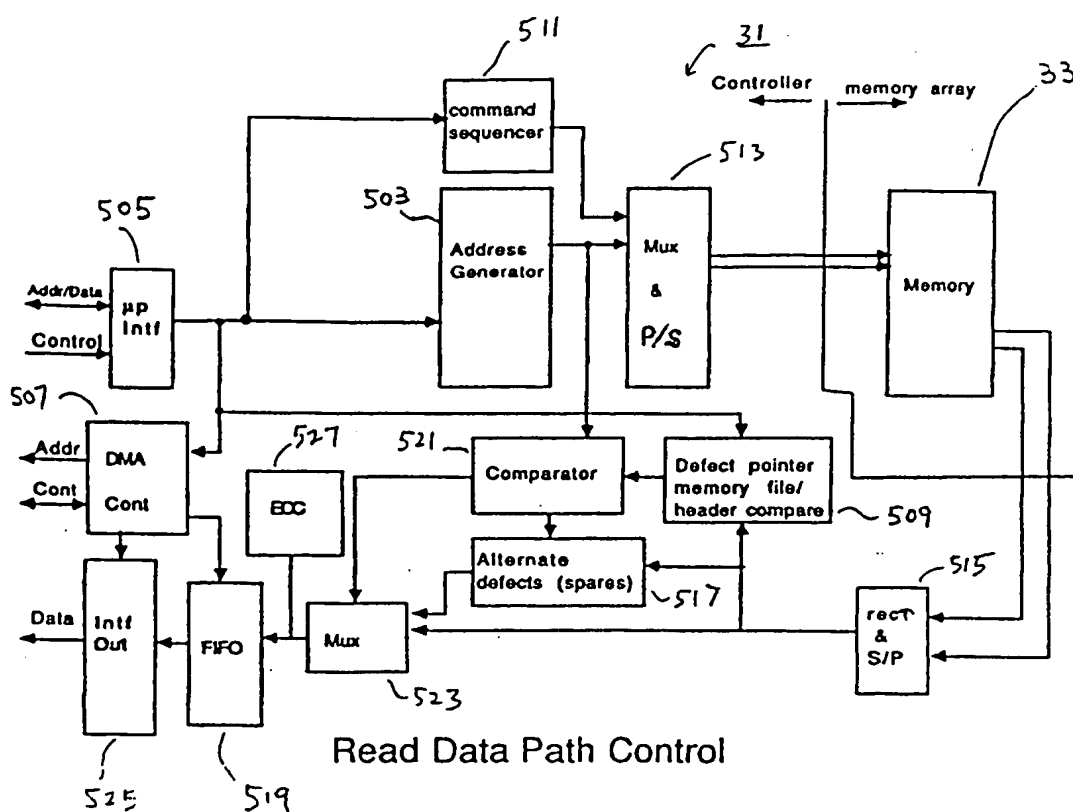


FIG. 4



SECTOR PARTITION

FIG-5



FIG_ 6

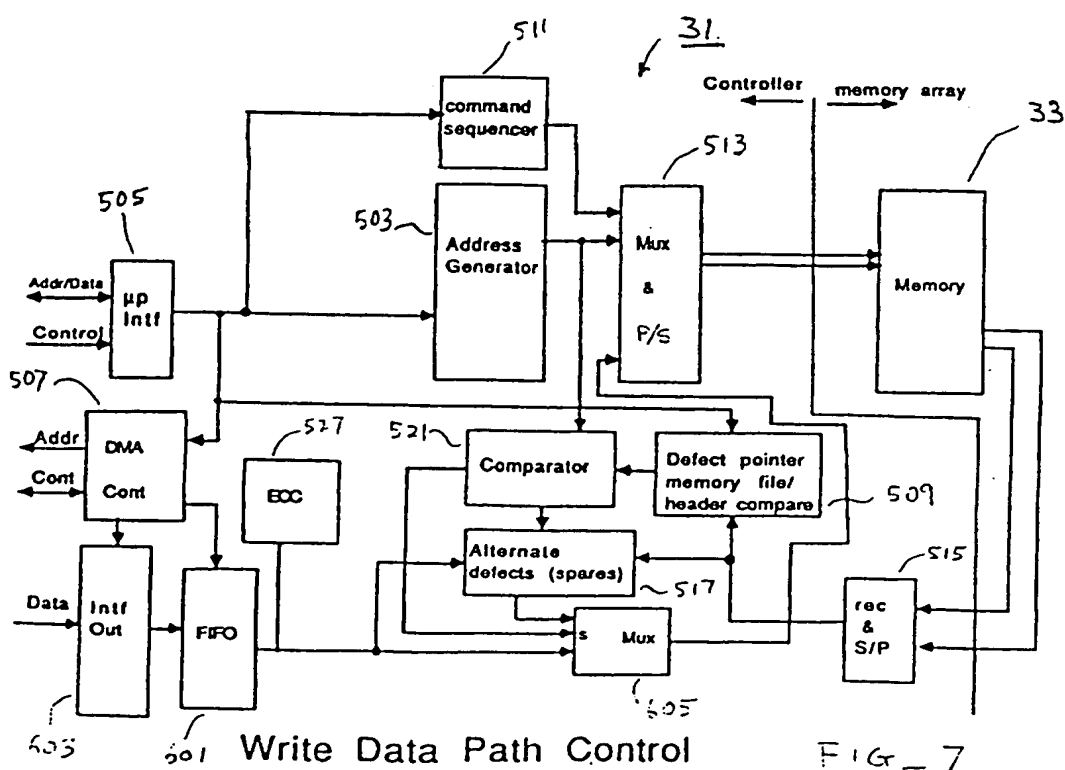


FIG - 7

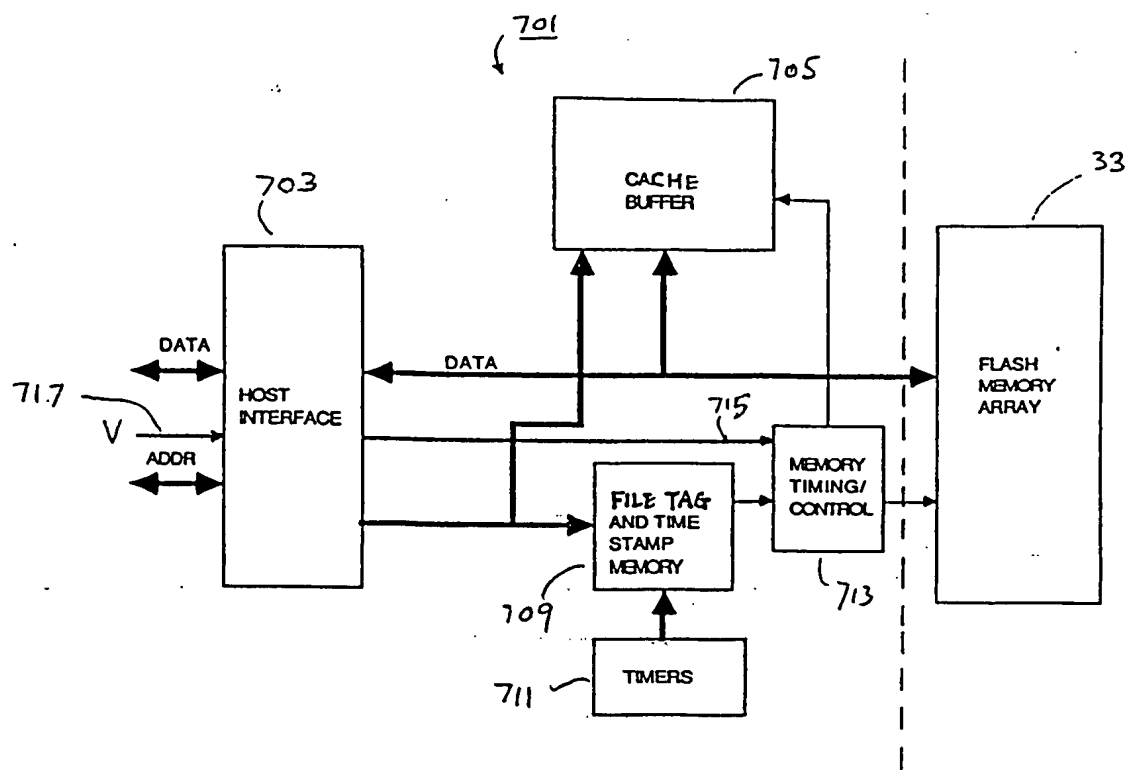


FIG. 8